

TO SEPARATE, HOLD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 10-051,916	GROUP/ART UNIT 2817	ATTACHMENT TO PAPER NUMBER 2		
NOTICE OF REFERENCES CITED				APPLICANT(S) Song et al.				
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	A	5278796	1-11-1994	Tillinghast et al.	365	222		
	B	6453218	9-17-2002	Vergis	365	222		
	C	6483764	11-18-2002	Chen Hsu et al.	365	222		
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	L	EP 325026	7-25-1989	European	Hanbury	-	-	
	M							
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	R	Millman "Micro-Electronics Digital and Analog Circuits and Systems" McGraw-Hill Book Company 1979, pp 276-286						
	S	Kagenish: et al. "Low Power self refresh mode DRAM with temperature detecting circuit" Symposium on VLSI circuits						
	T	1993 Digest of Technical Papers pp 43-44						
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EXAMINER SHINGLETON				DATE 4-23-2003				
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)								